

# Danube River Test Waver

by LibreSilicon

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This is the automatically generated documentation and guide line for the test structures in the GDSII file, generated by this script.

This is phase two of the reverse engineering/verification of the manufacturing process.

The structures have been generated assuming basic flags and settings for the pad and size from "configs/kacst-ls1u.cfg" as well as variables defined in "configs/ls1u.py".

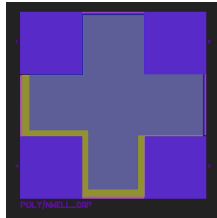
Those values need to be verified by checking under the microscope, whether the defects have gone away and measuring what the difference between predicted values and measured values is

## 1 Capacitors

All the capacitors structures for the various available layer pairs, as defined in the configuration are being shown below. They are being measured with a 4 probe station, by applying a constant current over two of the probes.

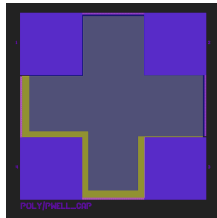
This is called a Kelvin structure.

### 1.1 Capacitor POLY/NWELL\_CAP



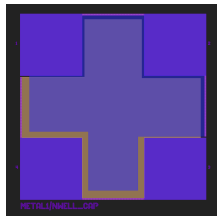
The target value of this capacitor is

### 1.2 Capacitor POLY/PWELL\_CAP



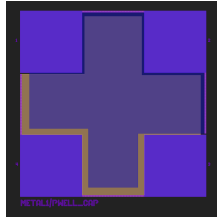
The target value of this capacitor is

### 1.3 Capacitor METAL1/NWELL\_CAP



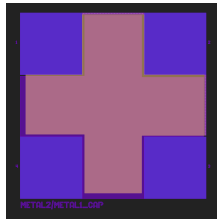
The target value of this capacitor is

#### 1.4 Capacitor METAL1/PWELL\_CAP



The target value of this capacitor is

#### 1.5 Capacitor METAL2/METAL1\_CAP



The target value of this capacitor is